

蔡宗叡 老師

現職 光電與通訊學系 助理教授

學歷 長庚大學電子工程研究所博士

專長1 固態電子

專長2 光學工程

專長3 半導體

專長4 顯示技術

教師研究成果資料明細



SCI、SSCI、A&HCI、EI、TSSCI期刊論文

1. 楊紹明(Shao-Ming Yang)、許健(Gene Sheu)、蔡宗叡(Jung-Ruey Tsai)
,2010-11, (已刊登)

ICSICT-2006: 2006 8th International Conference on Solid-State and Integrated
Circuit Technology, Proceedings 1卷3期:1838頁~1840頁

A 5V/200V SOI Device with a Vertically Linear Graded Drift Region

2. 楊紹明(Shao-Ming Yang)、許健(Gene Sheu)、蔡宗叡(Jung-Ruey Tsai)
,2011-08, (已刊登)

ICEMI 1卷期:85頁~88頁

Application of Multi-Lateral Double Diffused Field Ring in Ultrahigh-Voltage Device
MOS Transistor Design

3. 蔡宗叡(Jung-Ruey Tsai)、許健(Gene Sheu)、楊紹明(Shao-Ming Yang)
,2011-08, (已刊登)

ICEMI 1卷期:235頁~238頁

Analysis of Si₃N₄ passivation effect by self-consistent electro-thermal-mechanical
simulation in AlGa_N/Ga_N heterostructure HEMTs

4. 楊紹明(Shao-Ming Yang)、許健(Gene Sheu)、蔡宗叡(Jung-Ruey Tsai)
,2011-08, (已刊登)

ICEMI 1卷期:239頁~242頁

Effects of SiO₂ passivation on AlGa_N/Ga_N HEMT by self-consistent electro-thermal-
mechanical simulation

5. 許健(Gene Sheu)、蔡宗叡(Jung-Ruey Tsai)*、楊紹明(Shao-Ming Yang)
,2011-08, (已刊登)

The Ninth International Conference on Electronic Measurement & Instruments 4卷
期:5頁~9頁

Improvement of Electrical Characteristics in LDMOS by the Insertion of PBL and
Gate Extended Field Plate Technologies

6. 許健(Gene Sheu)、蔡宗叡(Jung-Ruey Tsai)*、楊紹明(Shao-Ming Yang)
,2011-08, (已刊登)

ICEMI 4卷期:5頁~9頁

Improvement of Electrical Characteristics in LDMOS by the Insertion of PBL